

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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JUL 30 2004

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In re application of  
GEORG FARKASAtty. Docket  
CH 000018

Serial: 09/932,086

Group Art Unit: 2133

Filed: 08/17/2001

Examiner: ENG, MARSHALL S J

ARRANGEMENT FOR TESTING INTEGRATED CIRCUITS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450RESPONSE UNDER 37 C.F.R. 1.116

Sir:

Responsive to the Office Action of June 4, 2004, please amend this application as follows:

## IN THE CLAIMS

1. (Canceled)
2. (Previously amended) The integrated circuit of claim 11, wherein the means for receiving test results comprises a test response analysis unit for compressing test